

Four-channels Low Jitter Clock Generator

MCG-200

- Multi-channels low jitter clock generation
- All channels in synchronization
- Independent frequency setting on each channel
- Built-in internal reference clock



Specifications

Category	Parameter	Specification			
		Min.	Typ.	Max.	Unit
Input Characteristics (External Clock is not required but customer can input own external frequency to synchronize the clk generator module)	External Clock Frequency	9.9	10.0	10.1	MHz
	External Clock Amplitude	200	450	1000	mVpp
	External Clock Duty Cycle		50		%
	External Clock Input Resistance (SE)		50		Ohm
Output Characteristics	Output Clock Frequency		100Hz ~ 1000MHz		
	Frequency Setting Resolution		10		Hz
	Output Clock Amplitude (AC Coupling, Single Ended)	300	450		mVpp
	Output Clock Shape		Rectangular		
	Rise/Fall Time (20% to 80%)		150	250	ps
	Duty Cycle		50	55	%
	Clock Jitter (RMS)		0.15	0.20	ps
	Output Impedance		50		Ohm
Input/Output Ports	External Clock Synchronize Input Port		Advance SMA x 1		
	Number of Clock Output Ports		Advance SMA x 4		
	Trigger Output Port		Advance SMA x 1		
	Interface ⁽¹⁾		MIL Connector , 14 Pins , 2.5mm		
	Power Supply ⁽¹⁾⁽²⁾		DC+5.0, 1A		
Physical	Dimension (W x H x D) (Exclude Protrusion)		75 x 15 x 75		mm
	Weight		100		g

Note: (1) The micro USB port can be used for ASCII remote control and also provides +5V power supply to the unit.

(2) Customer can choose to power up the unit with external DC+5V.

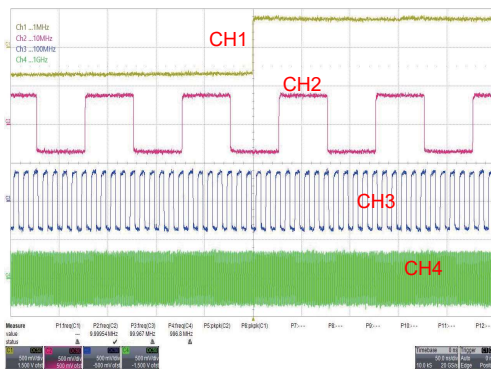


Fig 1: Clock Output CH1, CH2, CH3, CH4

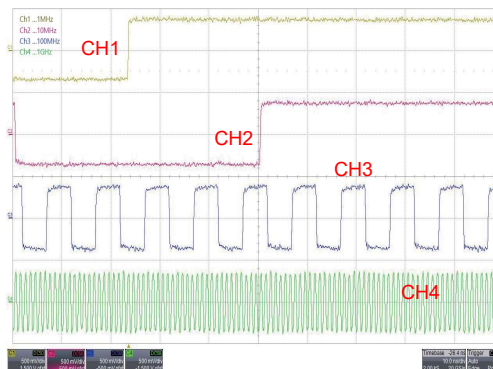


Fig 2: Clock Output CH1, CH2, CH3, CH4

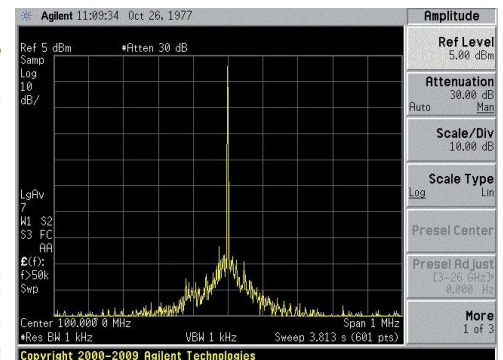


Fig 3: RF Spectrum with SNR >70dB

Ordering Information

MCG-200-0X

X: Number of Channels

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